

Andreas Ermedahl

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Date of birth: 5 August 1971



Curriculum Vitae – Dr. Andreas Ermedahl

Summary

Dr. Andreas Ermedahl is a senior researcher (docent) at the School of Innovation, Design and Engineering at Mälardalen University, Sweden. Dr. Ermedahl is also the coordinator of the industrial graduate school SAVE-IT, which involves more than 20 industrial PhD students. He has about 35 research publications in international workshops, conferences, journals, and books.

Dr. Ermedahl's main research focus is on Worst-Case Execution Time (WCET) analysis, and he is internationally well known for his work within this area. Dr. Ermedahl has been in the program committee for several international workshops and conferences. He has supervised a number of MSc students, he has supervised one PhD student to graduation, and he is currently assistant supervisor for four PhD students. He has been teaching several courses and developed many course assignments.

Dr. Ermedahl is one of the track coordinators of ALL-TIMES, an EU FP7 project. ALL-TIMES include both academic and industrial partners, such as AbsInt GmbH, TU Vienna, Rapita Systems, Gliwa GmbH and Symptavision GmbH. He also has an international research network through the ARTIST2 EU network of excellence. Dr. Ermedahl's research group has several industrial partners, including Arcticus Systems, CC-systems, Volvo CE, Enea and IAR Systems. Dr. Ermedahl has previously worked together with researchers at C-lab in Germany and Seoul National University in Korea.

WWW homepage

<http://www.idt.mdh.se/~ael01>

Publication homepage

http://www.mrtc.mdh.se/index.php?choice=publications&year=any&staff_id=0143

Working experiences in chronological order

November 2008 -

Docent lecture

- *Date:* 14 November 2008.
- *Affiliation:* School of Innovation, Design and Engineering, Mälardalen University

August 2008 –

Senior teacher (lector) at School of Innovation, Design and Engineering, Mälardalen University

July 2003 – August 2008

Researcher at School of Innovation, Design and Engineering, Mälardalen University

- Senior researcher active the Mälardalen WCET analysis research group, see <http://www.mrtc.mdh.se/projects/wcet> for more details
- Coordinator of the industrial research school SAVE-IT, see <http://www.mrtc.mdh.se/projects/save-it> for more details
- Assistant supervisor for five Ph.D. students, where one has so far graduated.
- Has been supervising more than ten M.Sc. students.
- Responsible teacher and course developer of an Embedded Systems course.
- Responsible teacher for Algorithm Analysis course.
- Lab assistant for courses in Linux, Unix and Automata Theory.

June 2003

Doctoral thesis

- *Date:* 3 June 2003.
Title: A Modular Tool Architecture for Worst-Case Execution Time Analysis.
Affiliation: Dept. of Information Technology, Uppsala University.
Supervisor: Prof. Hans Hansson.
Opponent: Prof. Reinhard Wilhelm

January 1996 – June 2003

PhD student at Dept. of Information Technology, Uppsala University

- Real-time systems as main research topic.
- Research focus on developing methods for static WCET analysis.
- Prototype tool for WCET analysis implemented based on these methods.
- Lecturing and assignment development for operating- and real-time system courses.

August 2000 - February 2001

Guest researcher at Seoul National University, Seoul, Korea

- Developed method to derive energy consumption model for embedded processors.

February 1995 – January 1996

Computer consultant, Ericsson Data, Kista, Stockholm.

June 1994 – January 1995

Master Thesis work, Erlang Systems, Elementel, Älvsjö, Stockholm.

- Developed model for doing event-driven simulation in the programming language Erlang.

Education

January 1996 – June 2003: Uppsala University, PhD studies, 160 points.

August 1990 – January 1995: Uppsala University, Computer science program, 160 points.

August 1987 – June 1990: 3-årig naturvetenskaplig linje, Lindeskolan, Lindesberg.

Additional academic merits

- General Chair and PC member of the RTIS2007 conference.
- Most productive researcher (in terms of number of publications) within the ASTEC competence centre at Uppsala University 1996-2006.
- PC member of the LCTES2006 conference.
- PC member of Embedded World 2006 conference.
- PC member of WCET2004, WCET2005 and WCET2008 workshops.