

Time	Name	Surname	Program	Report title	Opponent 1	Opponent 2	Supervisor 1	Supervisor 2	Examiner
08:00-08:45	Emil	Åberg	DVA503	MINIMIZING INTER-CORE DATA-PROPAGATION DELAYS IN PARTITIONED MULTI-CORE REAL-TIME SYSTEMS USING SCHEDULING HEURISTICS	David Pantzar	Christoffer Parkkila	Saad Mubeen	Matthias Becker	Mohammad Ashjaei
08:45-09:30	Alexander	Magnusson	DVA503	Integrating 5G Components into aTSN Discrete Event SimulationFramework	Ladan Pourvatan		Zenepe Satka	Mohammad Ashjaei	Saad Mubeen
	David	Pantzar	DVA503						
break									
09:40-10:25	Christian	Mortensen	DVA503	FAULT DETECTION IN MOBILEROBOTICS USING AUTOENCODERAND MAHALANOBIS DISTANCE	Emil Åberg	Aldin Berisa	Nandinbaatar Tsog		Thomas Nolte
10:25-11:10	Christoffer	Parkkila	DVA503	CLONE DETECTION IN MODEL-BASED DESIGN:AN EVALUATION IN THE SAFETY-CRITICALRAILWAY DOMAIN	Christian Mortensen		Eduard Enoiu	Muhammad Abbas	Thomas Nolte
break									
11:20-12:05	Ladan	Pourvatan	DVA503	TEST PROCESS ASSESSMENT OFINDUSTRIAL CONTROL SYSTEMS VIASAFETY STANDARDS	Alexander Magnusson		Wasif Afzal	Eduard Enoiu	Thomas Nolte